

About the Institute

National Institute of Technology (NIT), Silchar is one of the 30 NITs of India and was established in 1967 as a Regional Engineering College in Assam. In 2002, it was upgraded to the status of NIT and was declared as Institute of National Importance under the National Institutes of Technology Act, 2007. The climate of Silchar during February is very pleasant with normal temperature around 20-25°C and little cloudy.

NIT Silchar is a fully residential institution with nine hostels for boys and two hostels for girls. It has six engineering and five non-engineering branches offering undergraduate, post-graduate and doctoral degree in various fields of science, technology, and management.

About The Department

The Department of Electronics & Instrumentation Engineering was established in 2009. The department currently offers B.Tech degree in Electronics & Instrumentation and M. Tech. degree in Instrumentation with an intake of 60 and 10 respectively. The department has several well equipped laboratories for the regular course as well as research. The department offers doctoral degree in various fields like instrumentation, control, signal processing, human computer interface, communication, VLSI and drug delivery. The department is actively engaged in teaching as well as research.

How To Reach NIT Silchar

There are daily flights available from Kolkata and Guwahati. Taxi is available from Airport to NIT Silchar. Silchar is also well connected by road and train. The main bus terminus and railway station are 7 km and 9 km away from NIT Silchar respectively.

Important Dates

Registration Deadline: 30th January 2017.
(DD in original should be submitted on the first day of the workshop i.e. 14th February 2017)

Registration Procedure

External Students and Research Scholar: Rs 700/-
External Faculty: Rs 1500/-
Industry Person/Scientist/Engineer: Rs 2500/-

External participants have to make payment via DD in favour of **Director NIT Silchar**, Payable at **State Bank of India, NIT Silchar** Branch. Brochure and registration form can also be downloaded from the institute website:

<http://www.nits.ac.in>.

Scanned copy of Registration Form and Brochure along with DD and identity proof are required to be sent via email to (ranjayhazra87@gmail.com) on or before **30th January, 2017**.

The hard copy of original registration form and DD has to be submitted before the commencement of course at the registration desk.

Whereas, for **internal participants** there is no registration fees, but the scanned copy of filled up registration form needs to be sent by mail before due date and also the hard copy as mentioned above. Further seats are limited for the internal participants.

Accommodation and Travel

All the out station participants will be provided accommodation on payment basis in the Guest House/Hostels of the Institute. No TA and DA will be paid to the participants. Participants will be provided high tea and lunch during the days of the course i.e., from February 14 – February 18, 2017.

Participants will also be provided registration kit and course material.

*One Week
Short Term Course On*

Recent Trends in Communication, Signal Processing & VLSI (CSPV 2017) (Under TEQIP-II)

14th February to 18th February, 2017



Coordinators

Dr. Ranjay Hazra
Dr. Arun Kumar Sunaniya
Dr. Sandeep Kumar

Organized by

Department of Electronics and Instrumentation Engineering

National Institute of Technology Silchar
(An Institute of National Importance)

Silchar, Cachar, Assam, India, PIN-788010

www.nits.ac.in

About the Course

The advent of technology has industrialized the industrial and education society. Despite its success, there are several aspects which need significant improvements. A comprehensive interdisciplinary research and development effort is needed to address these issues. The course aims at providing comprehensive coverage of most emerging research topics in various fields related to Communication Systems, Signal Processing and VLSI design as they are not only interrelated but also interdependent. Signal processing is a ubiquitous technology which plays the central role in many different areas of applications, e.g., audio, video, image and speech processing, communications, healthcare, automotive safety and surveillance, safety and security of underground pipelines transporting water, oil and natural gas, and many more. Typical Signal processing algorithms are computation-intensive and may sometimes require FPGA for Co-processing. Whereas the VLSI is backbone for fabricating all type modern Integrated circuits

Objectives

The objective of this short term course program is to provide theoretical and practical background on most recent developments in field of Communication Systems, Signal Processing and VLSI design to the participants. The course aims in bringing academicians from institute of repute to provide insights through their talks and in depth discussions.

Eligibility

This program is open to industrial personnel, faculty members and students including research scholars, PG and UG students of recognized technical institutions.

Patron

Prof. R. Gupta
Director In-Charge, NIT Silchar

Chairman

Dr. Rajdeep Dasgupta
HOD, Dept. of E&IE

Advisory Members

Dr. S.H. Laskar, Associate Prof., Dept. of E&IE
Mr. L. Seban, Assistant Prof., Dept. of E&IE
Mrs. J. Hazarika, Assistant Prof., Dept. of E&IE
Mr. S. Sahoo, Assistant Prof., Dept. of E&IE
Dr. M. Khanra, Assistant Prof., Dept. of E&IE
Dr. S. Sahoo, Assistant Prof., Dept. of E&IE
Dr. M. K. Bera, Assistant Prof., Dept. of E&IE
Mr. T. Bachhar, Assistant Prof., Dept. of E&IE
Mr. K. Mondol, Assistant Prof., Dept. of E&IE
Mr. C. Dawo, Assistant Prof. Dept. of E&IE

Coordinators

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Resource Persons

- 1) Prof. C.K. Sarkar,
Department of Electronics Engineering, Jadavpur University.
- 2) Prof. R. Bhattacharjee,
Department of EEE, IIT Guwahati.
- 3) Prof. S. Bhattacharya,
Department of Electronics Engineering, IIT (ISM) Dhanbad.
- 4) Dr. M.K. Bhuyan,
Department of EEE, IIT Guwahati.
- 5) Dr. S. Biswas,
Department of CSE, IIT Guwahati.
- 6) Prof. A. Ghosh,
Machine Intelligence Unit, ISI Kolkata.
- 7) Dr. M.S. Manikandan,
School of Electrical Sciences, IIT Bhubaneswar.
- 8) Prof. S. Chakrabarti,
G.S. Sanyal School of Telecommunication, IIT Kharagpur
- 9) Dr. S.K. Mohammed,
Department of EEE, IIT Delhi.
- 10) Prof. D. Ghosh,
Department of ECE, IIT Roorkee.

**Short Term Course
On
Recent Trends in
Communication, Signal Processing and VLSI
(CSPV 2017)**

14th February to 18th February, 2017

Organized By:
**Department of Electronics and Instrumentation Engineering
National Institute of Technology, Silchar**

Registration Form

Name in Block Letters: _____

Gender: MALE / FEMALE

Designation: _____

Organization: _____

Address for Communication: _____

Mobile No: _____ Email: _____

Educational Qualification: _____

DD No: _____

Name of Bank: _____

Branch: _____

Required Accommodation (Paid): YES / NO

Signature of Applicant (With Date):